	Application No.	Applicant(s)
Notice of Allowability	09/964,809	OHYANAGI ET AL.
	Examiner	Art Unit
	DUNG A. LE	2818
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>Election dated 2/2/2005</u> .		
2. The allowed claim(s) is/are <u>1-5</u> .		
3.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 08), 7. ☑ Examiner's Amendr	te
		$\mathcal{N}_{\mathcal{L}}$

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Examiner's Amendment.

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Cancel Claims 6-7.

Change the title to read - Field Effect Transistor. --

DETAILED ACTION

Priority

Acknowledge is made of applicants' claim for foreign priority base on an application 2001-064318 filed in Japan on 03/08/2001.

It is noted that Applicants have filled a certified copy of said application as required by U.S.C 119, which papers have been placed of record in the file.

Oath/Declaration

The oath/declaration filed on 9/28/2001 is acceptable.

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Information Disclosure Statement

This office acknowledges of the following items from the Applicant:

Information Disclosure Statement (IDS) filed on October 12, 2000 and made of record. The references cited on the PTOL 1449 form have been considered.

Specification

The specification has been checked to the extent necessary to determine the presence of all possible minor errors. However, the applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Reason for allowance

Claims 1-6 are allowed. The following is an examiner's statement of reason for allowance:

Set of claims 1, 3-5: none of the references of record teaches or suggests the claimed Field effect transistor having punch-through stopper layer of the first conductivity type formed in contact with said source region at a location deeper than a junction between said source region of the second conductivity type and said semiconductor substrate of the first conductivity type in such a gate overlap structure that said punch-through layer penetrates from beneath an end face of said gate insulation film adjacent to said source

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electrode toward said drain electrode in contact with said gate insulation film and among other steps/limitations as claimed in independent claim 1.

Independent claim 2: none of the references of record teaches or suggests the claimed Fields effect transistor (in addition to other limitation as claimed in independent claim 1), comprising: a field relaxation layer of the second conductivity type formed in contact with said drain region in such a gate overlap structure that said field relaxation layer extends from beneath an end face of said gate insulation film adjacent to said drain electrode toward said source electrode in contact with said gate insulation film; wherein said field relaxation layer penetrates from beneath an end face of a gate electrode side wall toward said source electrode for a distance of at least 0.15µm.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dung A. Le whose telephone number is (571) 272-1784. The examiner can normally be reached on Monday-Tuesday and Thursday 6:00am- 4:00 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The central fax phone numbers for the organization where this application or proceeding is assigned are (571)272-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DUNG A. LE Primary Examiner
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